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Fliege, N.J.; Kikuchi, H.; Truong Q. Nguyen; Vaidyanathan, P.P.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Vol 45 , Issue: 8 , Aug. 1998
Pages:929 - 931

[\[Abstract\]](#) [\[PDF Full-Text \(32KB\)\]](#) **IEEE JNL****2 A high speed molybdenum gate MOS RAM**

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Iwata, A.; Kikuchi, H.; Uchimura, K.; Morino, A.; Nakajima, M.;
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Akazawa, Y.; Kikuchi, H.; Iwata, A.; Matsuura, T.; Takahashi, T.;
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Takezawa, M.; Kikuchi, H.; Suezawa, K.; Yamaguchi, M.; Ishiyama, K.; Arai, Magnetics, IEEE Transactions on , Volume: 34 , Issue: 4 , July 1998
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